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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

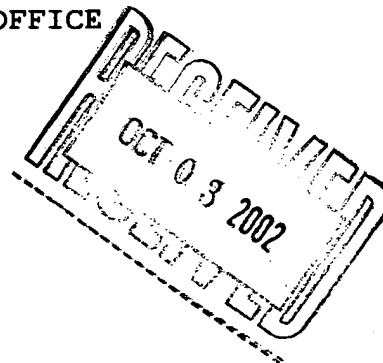
In re application of

Shin-itsu TAKEHASHI et al.

Serial No.: 10/088,264

Filed: March 18, 2002

For: THIN FILM TRANSISTOR AND METHOD FOR FABRICATING THE SAME



SECOND REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Office of Initial Patent Examination
Customer Service
Washington, D. C. 20231

Sir:

A corrected filing receipt is hereby requested in view of the error which appears in the original. For the convenience of the Patent and Trademark Office, attached is a photocopy of the original receipt on which the errors have been noted in red.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.

Date: September 16, 2002

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APPLICATION DATA SHEET

Application Information

Application number::

Filing Date:: 03/18/02

Application Type:: Regular

Subject Matter:: Utility

Suggested Classification::

Suggested Group Art Unit::

CD-ROM or CD-R?:: None

Number of CD disks:: None

Number of copies of CDs:: None

Sequence submission::

Computer Readable Form (CRF):: No

Number of copies of CRF:: None

Title:: THIN FILM TRANSISTOR AND
METHOD FOR FABRICATING THE
SAME

Attorney Docket Number:: OGOH:108

Request for Early Publication:: No

Request for Non-Publication:: No

Suggested Drawing Figure:: 1

Total Drawing Sheets:: 25

Small Entity:: No

Latin Name::

Variety denomination name::

Petition included:: No

Petition Type:: None

Licensed US Govt. Agency:: None

Contract or Grant Numbers:: None

Secrecy Order in Parent Appln.?:: No

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Initial 03/18/02

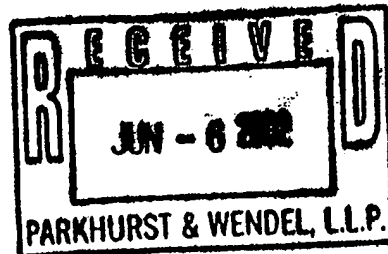


UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/088,264	03/18/2002	2812	2444	OGOH:108	25	41	17

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CONFIRMATION NO. 5130

FILING RECEIPT



OC000000008209230

Date Mailed: 05/31/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

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Foreign Applications

JAPAN H11-262119 09/16/1999
JAPAN H11-278687 09/30/1999

Projected Publication Date: Not Applicable, filed prior to November 29, 2000.

Non-Publication Request: No

Early Publication Request: No

Title

Thin-film transistor and method for ~~producing~~ ^{fabricating} the same

Please
correct →